System and Method for Producing an Integrated Circuit Layout Representation Todd P. Lukanc; H1776

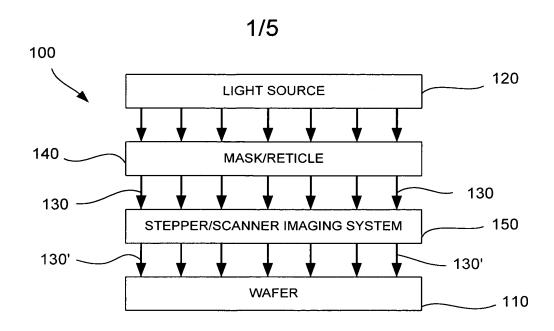


FIG. 1

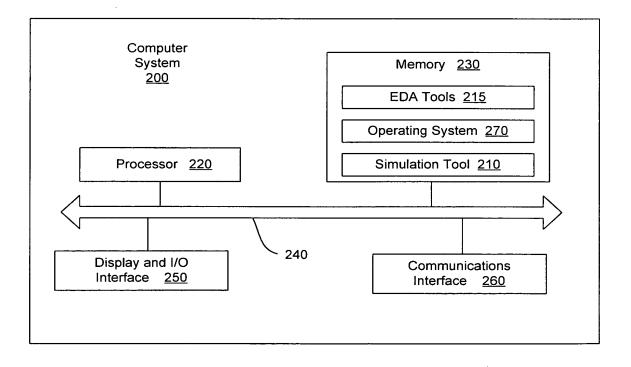


FIG. 2

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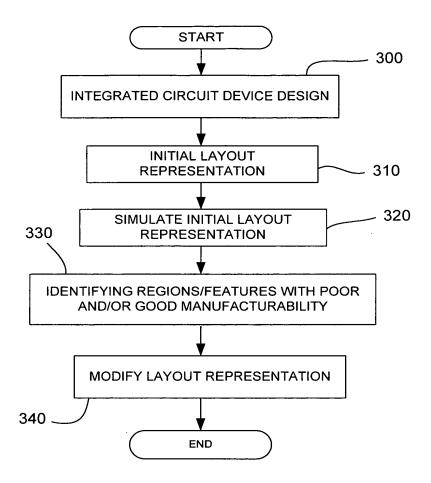
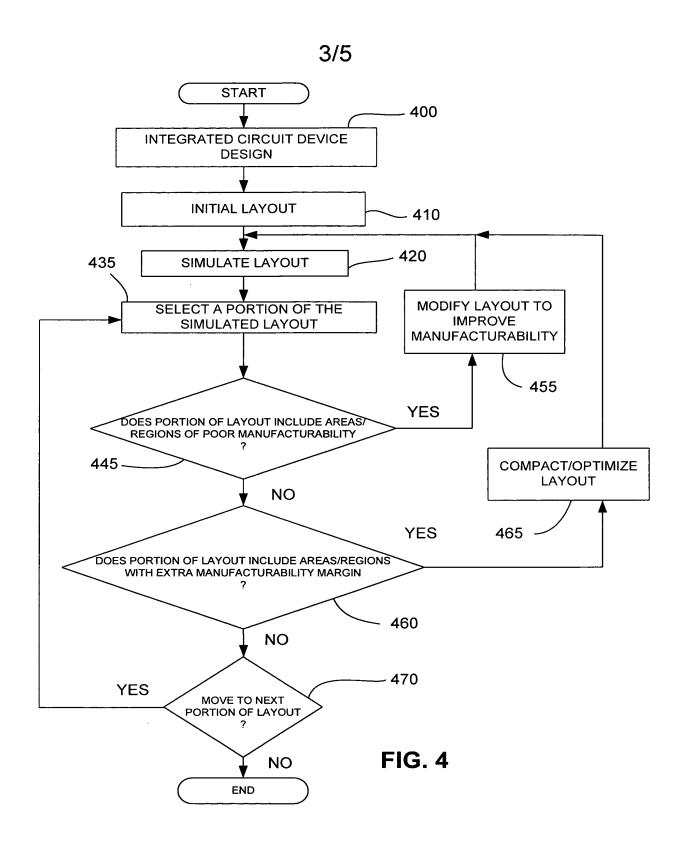
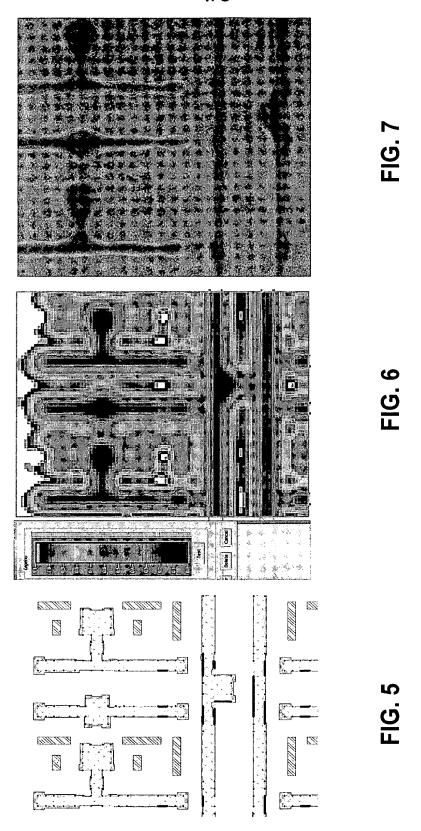


FIG. 3



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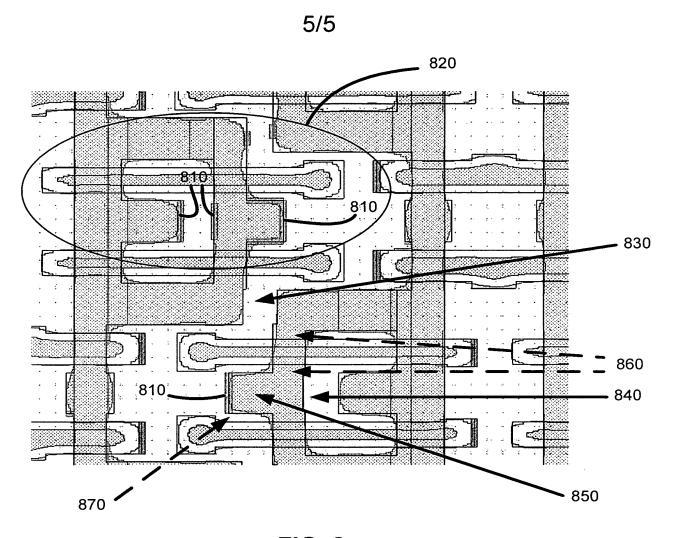


FIG. 8